

PATENT 0397-0439P

IN THE U.S. PATENT AND TRADEMARK OFFICE

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H. MATSUMOTO, et al.

Conf.:

5766

Appl. No.:

10/054,823

Group:

2826

Filed:

January 25, 2002

Examiner:

P. GREENE

For:

INTEGRATED SEMICONDUCTOR CIRCUIT DEVICE, PROCESS OF MANUFACCTURING THE SAME, IC MODULE AND IC CARD

LARGE ENTITY TRANSMITTAL FORM

Assistant Commissioner for Patents Washington, DC 20231

September 12, 2002

Sir:

Transmitted herewith is a Reply to Restriction/Election Requirement in the aboveidentified application.

	The enclosed document is being transmitted via the Certificate of Mailing provisions of 37 C.F.R. § 1.8.
	Petition for() month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$0.00 for the extension of time.
\boxtimes	No fee is required.
	A check in the amount of \$0.00 is enclosed.
	Please charge Deposit Account No. 02-2448 in the amount of \$0.00. A triplicate copy of this sheet is attached.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOŁASCH & BIRCH, LLP

Charles Gorenstein, #29,271

P.O. Box 747

Falls Church, VA 22040-0747

(703) 205-8000

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